

Notice of References Cited	Application/Control No. 10/803,042		Applicant(s)/Patent Under Reexamination ISHIDA ET AL.	
	Examiner Jeffrey D. Lane		Art Unit 2828	Page 1 of 1

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*	B	US-5,946,334	08-1999	Ema et al.	372/38.01
*	C	US-5,444,728	08-1995	Thompson, Marc T.	372/38.02
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	U	Trotter et al. ("A CMOS low voltage high performance interface" Trotter, J.D. Rekhi, S. Chava, V. Kale, P.C.; Microsyst. Prototyping Lab., Mississippi State Univ., MS, USA; ASIC Conference and Exhibit, 1994. Proceedings. Seventh Annual IEEE International ;19-23 Sept. 1994; pgs 44 – 48, Rochester, NY
	V	Canright (Practical design for controlled impedance" Canright, R.E., Jr. Martin Marietta Electron. Orlando, FL, USA; Electronic Components and Technology Conference, 1991. Proceedings. 41st : 11-16 May 1991; pgs. 370 – 377; Atlanta, GA).
	W	"Differential Amplifier" from http://www.wikipedia.org , accessed on 6/28/06
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.